|  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- |
|  | | | **SS2 FIRST TERM SCHEME** | | | |
| **WEEK** | **TOPIC** | **CONTENT** | | **OBJECTIVES** | **QUESTIONS** |
| 1 | CENTRAL PROCESSING UNIT (CPU) | 1. CENTRAL PROCESSING UNIT:   * Arithmetic and logic unit (ALU) * Control unit (CU)   2. FUNCTIONS:   * ALU * Control Unit | | Students should be able to:  1. List the components of the CPU  2. State two functions of the ALU and control unit | 1. Explain term CPU  2. List the components of the CPU  3. State two functions of the ALU  State two functions of the control unit  4. Using diagram explain the components of Central Processing Unit  5. State five differences between ALU & CU |
| 2 | MEMORY UNIT | 1. TYPES OF MEMORY:  2. DESCRIPTION:   * Primary Memory * Secondary Memory   3. Differences between primary and secondary memory  4. Units of storage  5. Conversion from one unit to the other  6. Comparison of Auxiliary storage Devices under:   * Size * Speed * Technology (optical, magnetic and semiconductor) | | Students should be able to:  1. State the types of memory  2. Describe primary and secondary memory  3. State differences between primary and secondary memory.  4. State the units of storage  5. Convert from one unit to the other  6. List auxiliary storage devices  Compare auxiliary storage devices. | 1. State two types of main memory  2. List three examples of secondary storage  3. Convert 1.44MB to bytes  4. Draw and label floppy disk.  5. State two differences between floppy disk and compact disk (CD) |
| 3 | LOGIC CIRCUIT I (Standard single logic gate | 1. Definition of logic gate.  2. Types of logic gates: AND, NOT, OR,  3. Symbol. of each logic Kate:  4. Input/output signals for: AND, NOT, OR gates  5. Truth table construction for: AND. NOT, OR  6. Equation for: AND, NOT, OR gates  7. Used of logic gates | | Students should be able to:  1. Define logic gate.  2. List types of logic gate  3. Identify symbols of each logic gate  4. Recognize and state the in/output signals of each logic gate  5. Construct table for each logic gate.  6. Write simple equation for each logic gate  7. State the differences in the standard logic gates.. | 1. What is Logic gate  2. With relevant diagrams describe the three types of standard logic gates  3. Construct a truth table for  i. AND gate ii. OR gate iii. NOT  4. State two differences between AND gate and OR gate  5. State the equations for AND, OR and NOT gates  6. State any five uses of logic gates |
| 4 | Logic Circuits II (Alternative logic gates) | 1. Description of alternative logic gate.  2. Types of alternative logic gates:  NAND, NOR  3. Symbols of each logic gate:  4. Input/output signals for:  NAND, NOR gates  5. Truth table construction for:  NAND, NOR,  6. Equation for: NAND, NOR, gates  7. Uses of logic gates:  8. Construction of simple comparator | | Students should be able to:  1. Describe alternative logic gates.  2. List types of alternative logic gates  3. Identify symbols of  each logic gate  4. Recognize and state the in/output signals of each logic gate  5. Construct truth table for each logic gate.  6. Write simple  7. equation for each logic gate  8. State the uses of logic gates  9. Construct a simple  comparator using XOR | 1. Describe alternative logic gate  2. With relevant diagrams describe NAND & NOR  3. Construct a truth table for  i. NAND gate ii. NOR gate  4. State two differences between NAND gate and NOR gate  5. State the equations for NAND & NOR gates  6. Construct a simple comparator using XOR and XNOR gate |